

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on line 18 of page 1 and ending on line 24 of page 1 as follows:

This application is related to ~~Patent Application No. 09/608,097 having attorney docket no. M-8321-US~~ United States Patent No. 6,735,197 filed June 30, 2000, and entitled "CONCATENATION DETECTION ACROSS MULTIPLE CHIPS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

Please amend the paragraph beginning on line 1 of page 2 and ending on line 14 of page 2 as follows:

This application is related to Patent Application No. 09/607,912 ~~having attorney docket no. M-8320-US~~ filed June 30, 2000, and entitled "PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE CHIPS PROCESSORS," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.

This application is related to Patent Application No. 09/608,461 ~~having attorney docket no. M-8340-US~~ filed June 30, 2000, and entitled "CHANNEL ORDERING FOR COMMUNICATION SIGNALS SPLIT FOR MATRIX SWITCHING," having Douglas E. Duschatko, Lane Byron Quibodeaux, Robert A. Hall, Andrew J. Thurston as inventors. This application is assigned to Cisco Technology, Inc., the assignee of the present invention, and is hereby incorporated by reference, in its entirety and for all purposes.